

S14137-01CR

## Compact 16-element APD array suitable for various light level detection (parallel output)

The S14137-01CR is a compact optical device that integrates a 16-element APD array and a preamp. The incident light pulse is converted into a voltage pulse, and then the converted pulse is output. It has a built-in DC feedback circuit for reducing the effects of background light. The parallel output 16-element array enables simultaneous measurement of all pixels.

### Features

- ➔ 16 ch parallel output
- ➔ High-speed response: 180 MHz
- ➔ Reduced background light effects
- ➔ Small waveform distortion when excessive light is incident

### Applications

- ➔ Distance measurement (e.g., LiDAR)

### Structure

Parameter	Symbol	Specification	Unit
Detector	-	Si APD array	-
Photosensitive area (per element)	A	0.15 × 0.43	mm
Element pitch	-	0.5	mm
Number of elements	-	16	-
Number of output	-	16	-
Package	-	Plastic	-

### Absolute maximum ratings

Parameter	Symbol	Condition	Value	Unit
Supply voltage (for preamp)	V <sub>CC</sub> max		4.5	V
Reverse voltage (for APD)	V <sub>APD</sub>		0 to V <sub>BR</sub>	V
Photocurrent (DC)	I <sub>L</sub> max		0.2	mA
Incident pulse light level	P <sub>pulse</sub>		1	W
Operating temperature	T <sub>opr</sub>	No dew condensation*1	-40 to +105	°C
Storage temperature	T <sub>stg</sub>	No dew condensation*1	-40 to +125	°C
Soldering temperature*2	T <sub>sol</sub>		260 (twice)	°C

\*1: When there is a temperature difference between a product and the surrounding area in high humidity environment, dew condensation may occur on the product surface. Dew condensation on the product may cause deterioration in characteristics and reliability.

\*2: Reflow soldering, JEDEC J-STD-020 MSL 3, see P.8

Note: Exceeding the absolute maximum ratings even momentarily may cause a drop in product quality. Always be sure to use the product within the absolute maximum ratings.

**Electrical and optical characteristics (Ta=25 °C, Vcc=3.3 V, AC coupling + 50 Ω, without DC light, per pixel)**

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Spectral response range	$\lambda$		420 to 1150			nm
Peak sensitivity wavelength	$\lambda_p$	M=100	-	840	-	nm
Photosensitivity	S	$\lambda=905$ nm, M=1	-	0.5	-	A/W
Breakdown voltage	V <sub>BR</sub>	I <sub>D</sub> =100 μA	150	175	200	V
Temperature coefficient of breakdown voltage	ΔTV <sub>BR</sub>		-	1.1	-	V/°C
Sensitivity uniformity	-	M=50 (average over all pixels)	-30	-	+30	%
Dark current	I <sub>D</sub>	M=50	-	0.1	1	nA
Transimpedance amplifier gain	G		-	1.4	-	kV/A
Current consumption	I <sub>C</sub>	Total sum of pixels	-	70	90	mA
Low cutoff frequency	f <sub>cl</sub>		-	4	-	MHz
High cutoff frequency	f <sub>ch</sub>		140	180	-	MHz
Equivalent input current noise <sup>*3</sup>	e <sub>n</sub>	f=100 MHz, M=50	-	3.75	5.5	pA/Hz <sup>1/2</sup>
Output voltage level	-		0.65	1.15	1.65	V
Output impedance	Z <sub>o</sub>	f=100 MHz	-	50	80	Ω
Maximum output voltage amplitude	V <sub>p-p</sub> max		0.2	0.5	-	V
Supply voltage	V <sub>cc</sub>		3.135	3.3	3.465	V
Crosstalk <sup>*4</sup>	-	FWHM=4 ns, =1 mW	-	-40	-	dB

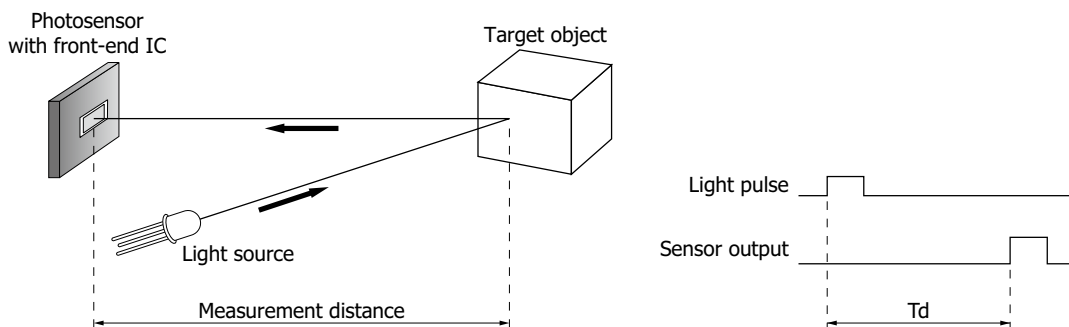
\*3: Reference values defined by simulation or characteristic evaluation

\*4: Crosstalk [dB] =  $20 \log_{10} \left( \frac{\text{Crosstalk [V]}/\text{TIA gain [V/W]}}{\text{Incident pulse light level [W]}} \right)$

**Distance measurement method**

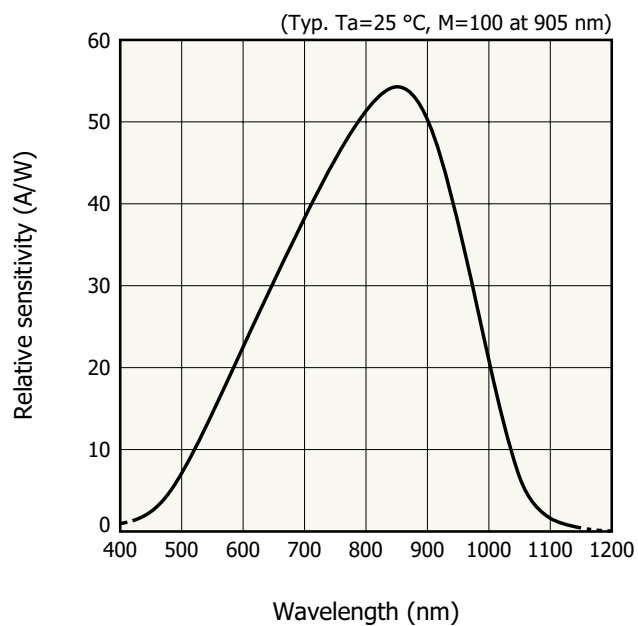
Distance L is calculated from the speed of light c and the time difference T<sub>d</sub> between the light source's light emission timing and sensor output.

$$L = (1/2) \times c \times T_d$$

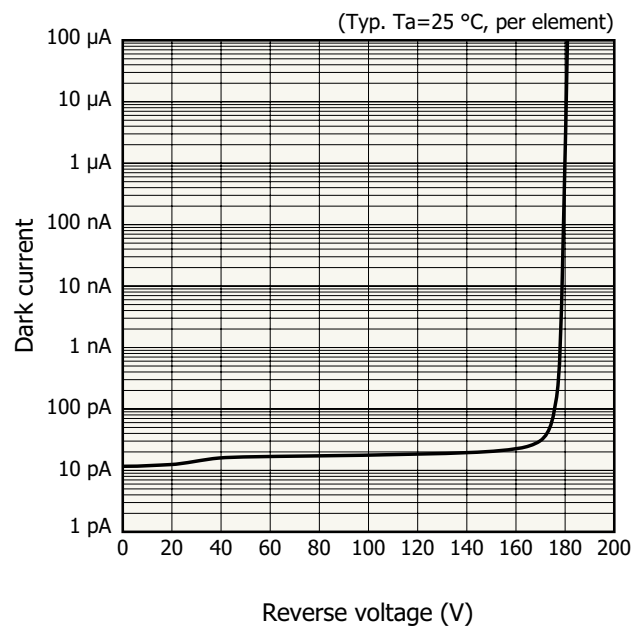


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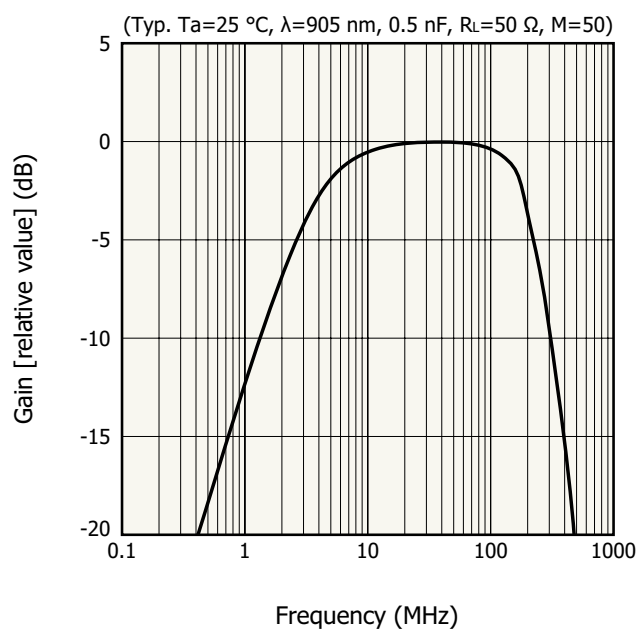
### Spectral response



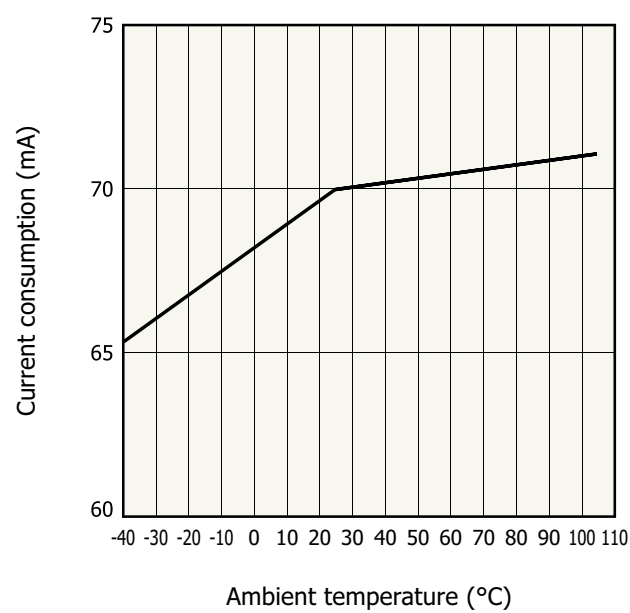
### Dark current vs. reverse voltage



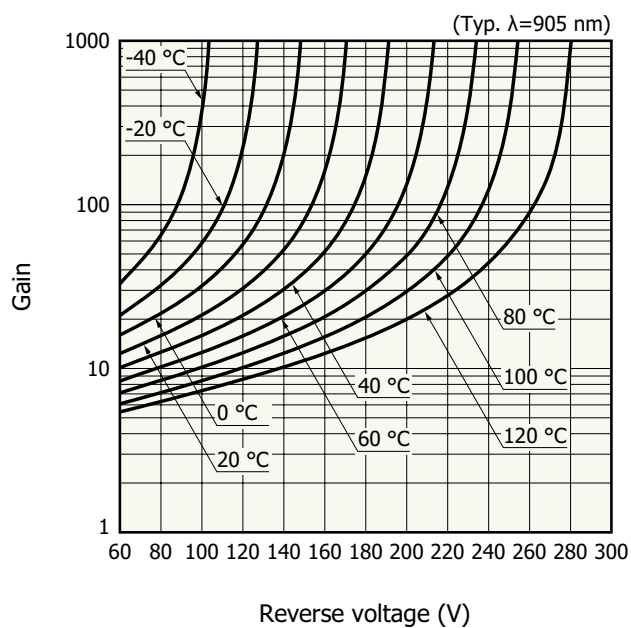
### Frequency characteristics



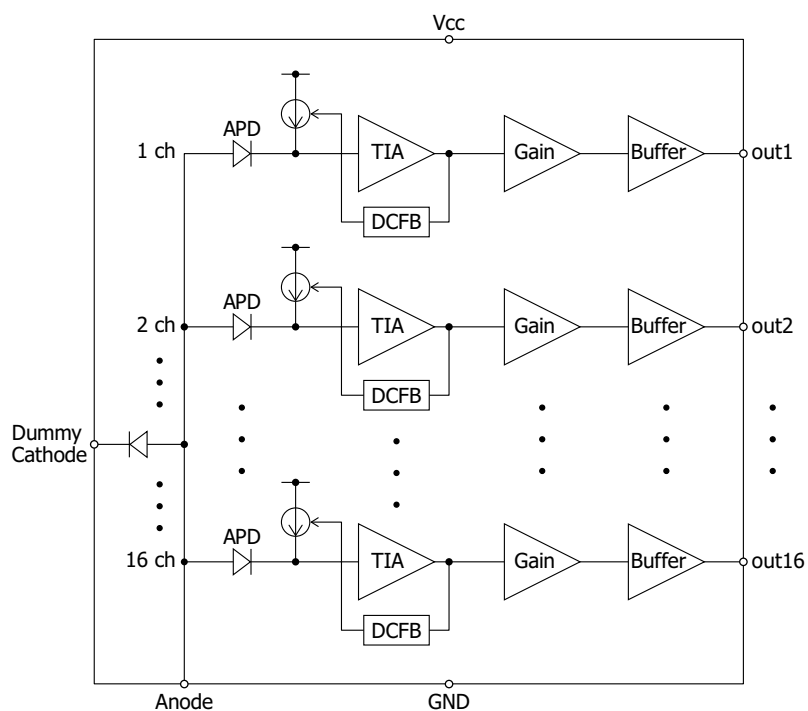
### Current consumption vs. ambient temperature (typical example)



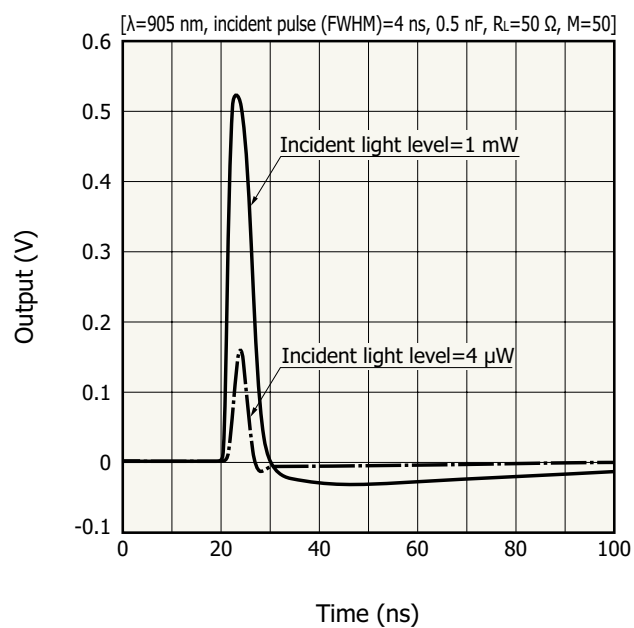
## Gain vs. reverse voltage



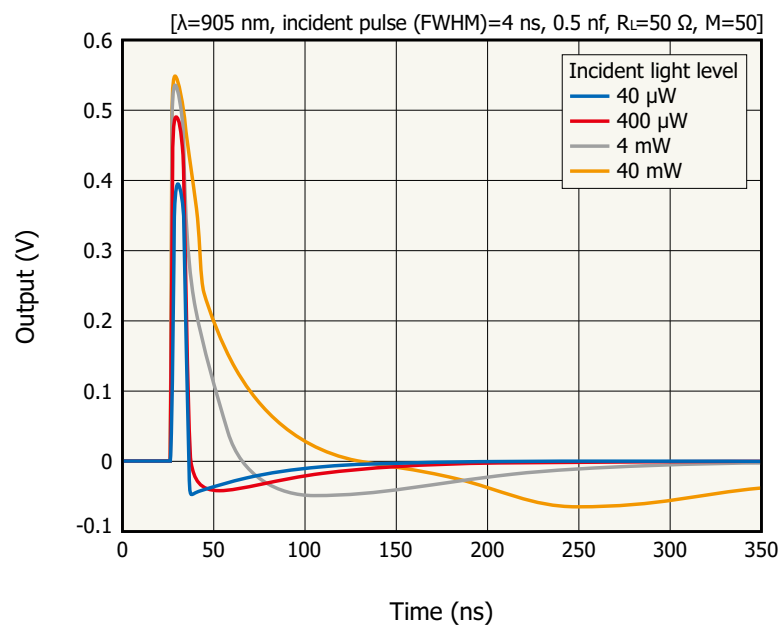
## Block diagram



## Output waveform examples

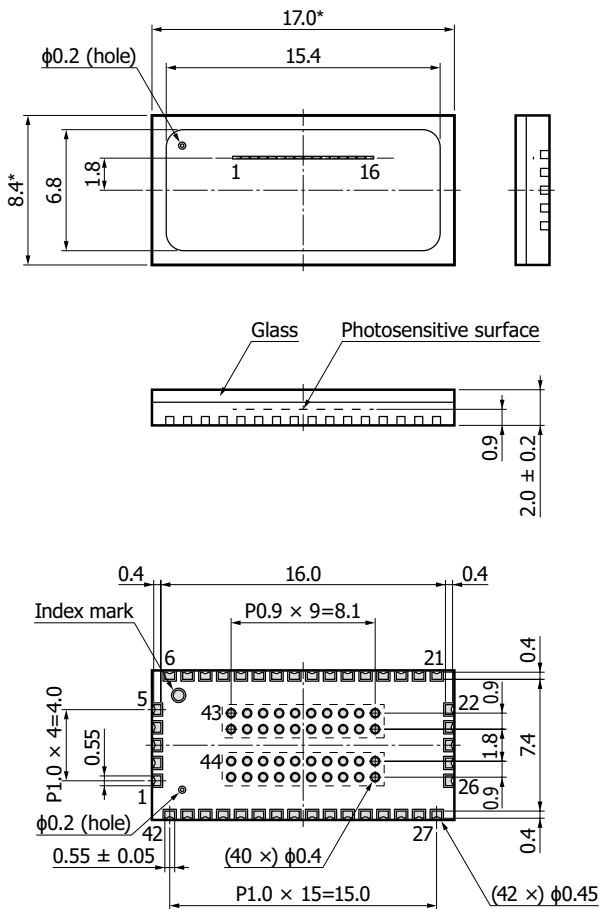



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### Dimensional outline (unit: mm)



Tolerance unless otherwise noted  $\pm 0.25$ ,  $\pm 2.5^\circ$   
 Chip position accuracy with respect to package  
 dimensions marked \*  
 $X, Y \leq \pm 0.3$ ,  $\theta \leq \pm 2.5^\circ$   
 Au plating

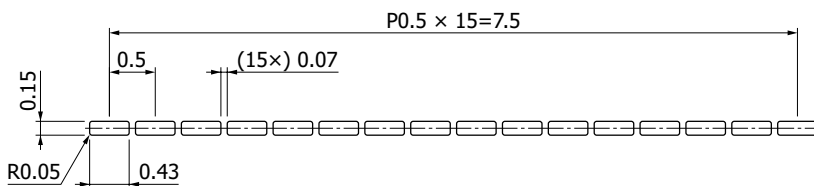
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## Pin connections

Pin no.	Function	Pin no.	Function
1	NC	23	GND
2	Vcc	24	GND
3	GND	25	Vcc
4	GND	26	NC
5	GND	27	Dummy cathode
6	out1	28	NC
7	out2	29	NC
8	out3	30	NC
9	out4	31	Anode
10	out5	32	Anode
11	out6	33	Anode
12	out7	34	Anode
13	out8	35	Anode
14	out9	36	Anode
15	out10	37	Anode
16	out11	38	Anode
17	out12	39	NC
18	out13	40	NC
19	out14	41	NC
20	out15	42	Dummy cathode
21	out16	43	GND
22	GND	44	NC

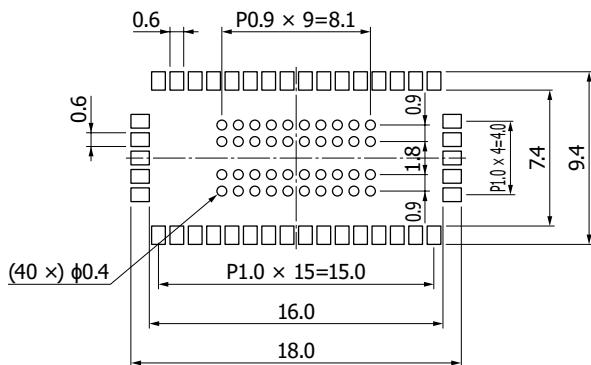
Note: Leave NC (1, 26, 28 to 30, 39 to 41) open; Do not connect to Vcc or GND.

**Enlarged view of photosensitive area (unit: mm)**



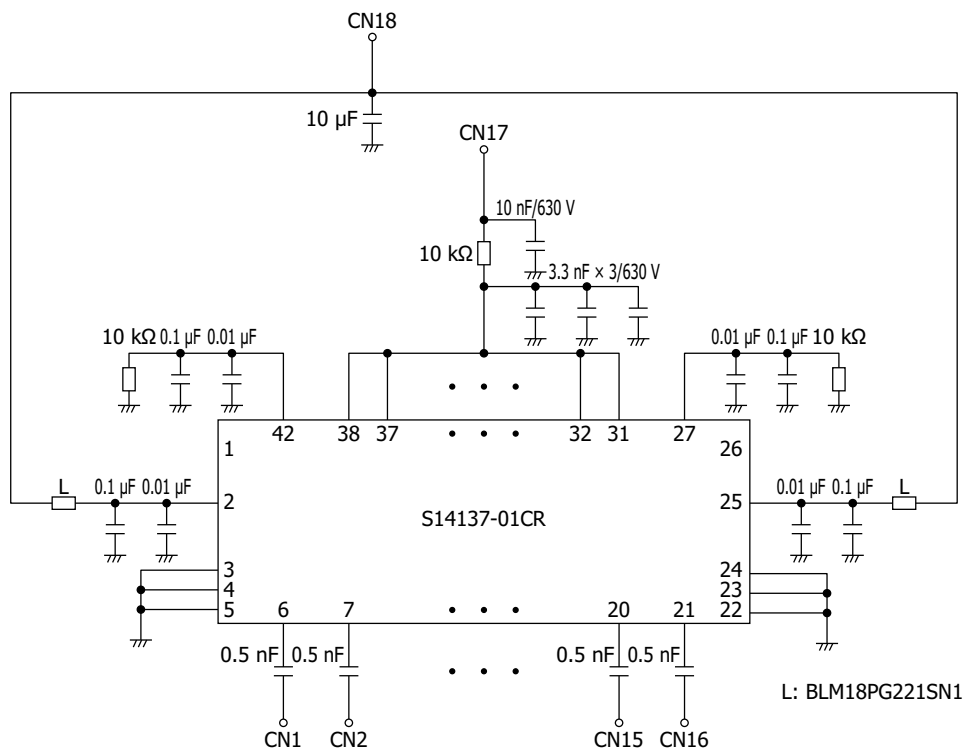
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Recommended land pattern (unit: mm)



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Application circuit example (50 Ω system, evaluation kit: C14779-03)



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### Handling of the temperature characteristics of the APD gain

The gain of the APD built into the photosensor with front-end IC varies depending on the temperature. The following two methods are available for handling this issue in using the sensor over a wide temperature range.

① Temperature compensation method that controls reverse voltage according to temperature changes

Place temperature sensor such as a thermistor near the APD to measure the temperature of the APD. The reverse voltage after APD temperature correction is expressed by the following equation using temperature T of APD.

$$V_R (\text{after temperature correction}) = V_R (\text{at } 25^\circ\text{C}) + (T - 25) \times \Delta TV_{BR}$$

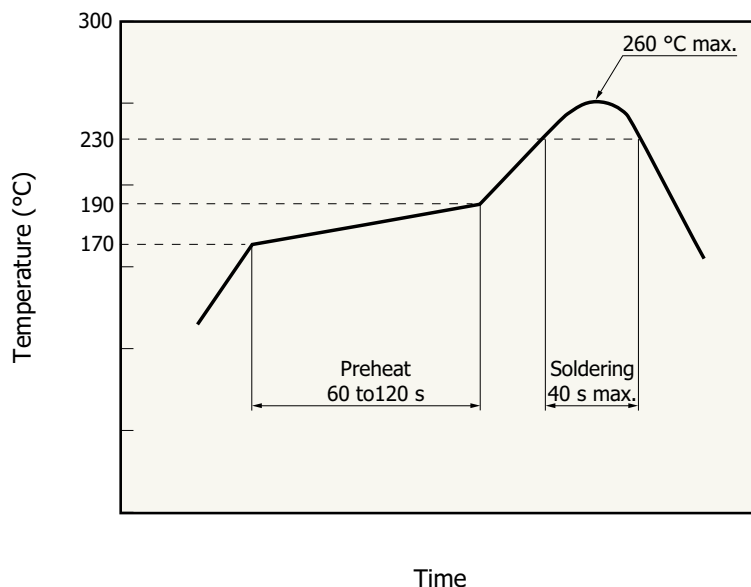
② Temperature control method to keep APD temperature constant

A TE-cooler or an equivalent device is used to keep APD temperature constant.

### Precautions

- Do not do cleaning or vapor phase soldering, as cleaning liquid or water may get inside the package through the air hole on the bottom of the package.
- Apply high voltage to the anode terminal. Beware of electric shock.
- Apply negative voltage with respect to GND (-100 V, etc.) to the anode terminal.
- The top of the package is glass. Be careful not to pinch it too hard with metal tweezers, as this can cause cracks or flakes.

### Recommended reflow soldering conditions



KPIC0346EA

- This product supports lead-free soldering. After unpacking, store it in an environment at a temperature of 30 °C or less and a humidity of 60% or less, and perform soldering within 24 hours.
- The effect that the product receives during reflow soldering varies depending on the circuit board and reflow oven that are used. When you set reflow soldering conditions, check that problems do not occur in the product by testing out the conditions in advance.



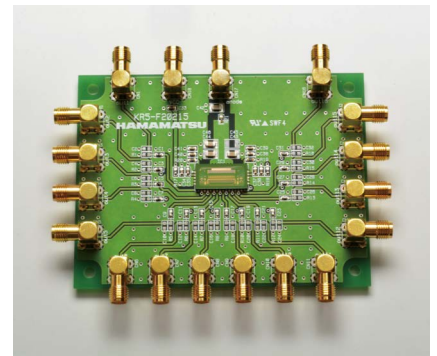
## Related information

[www.hamamatsu.com/sp/ssd/doc\\_en.html](http://www.hamamatsu.com/sp/ssd/doc_en.html)

- Precautions
  - Disclaimer
  - Precautions / Metal, ceramic, plastic package products
  - Precautions / Surface mount type products
- Catalogs
  - Selection guide / Photo IC

### Evaluation kit for photosensor with front-end IC C14779-03

Evaluation kit equipped with S14137-01CR is available. Refer to the application circuit example (P.7) for the equivalent circuit. Contact us for detailed information.



Information described in this material is current as of November 2024.

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