

NMOS linear image sensor



S3901/S3904-F series

NMOS linear image sensors with fiber optic windows

NMOS linear image sensors are self-scanning photodiode arrays designed specifically as detectors for multichannel spectroscopy. The scanning circuit is made up of N-channel MOS transistors, operates at low power consumption and is easy to handle. Each photodiode has a large active area, high sensitivity yet very low noise, delivering a high S/N even at low light levels. S3901/S3904-F series are current-output type NMOS linear image sensors with fiber optic windows and feature superior output linearity and wide dynamic range. The fiber optic windows allow efficient optical coupling to an image device such as image intensifiers suitable for low-light-level detection.

The photodiodes of S3901-F series have a height of 2.5 mm and are arrayed in a row at a spacing of 50 µm. The photodiodes of S3904-F series also have a height of 2.5 mm but are arrayed at a spacing of 25 µm. The photodiodes are available in 2 different pixel quantities for each series: 256 (S3901-256F), 512 (S3901-512F, S3904-512F), 1024 (S3904-1024F).

Features

- → Wide active area
 - Pixel pitch: 50 µm (S3901-F series) 25 μm (S3904-F series)

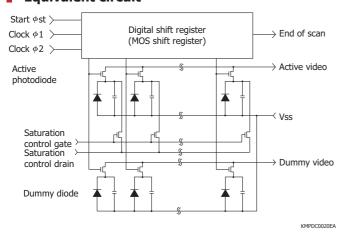
Pixel height: 2.5 mm

- Low dark current and high saturation charge allow a long integration time and a wide dynamic range at room temperature
- Excellent output linearity and sensitivity spatial uniformity
- Lower power consumption: 1 mW max.
- Start pulse and clock pulses are CMOS logic compatible

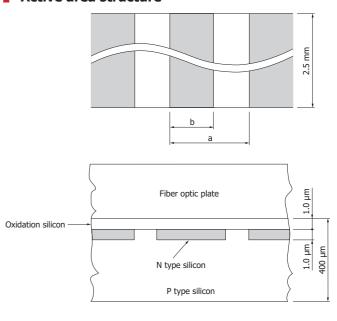
Applications

- **■** Multichannel spectrophotometry
- → Image readout system

Equivalent circuit



Active area structure



S3901-F series: a=50 μm, b=45 μm S3904-F series: a=25 um, b=20 um

KMPDA0131EA

■ Absolute maximum ratings

Parameter	Symbol	Value	Unit
Input pulse (φ1, φ2, φst) voltage	Vφ	15	V
Power consumption*1	Р	1	mW
Operating temperature*2	Topr	-40 to +65	°C
Storage temperature	Tstg	-40 to +85	°C

^{*1:} V ϕ =5.0 V

Shape specifications

Parameter	S3901-256F	S3901-512F	S3904-512F	S3904-1024F	Unit
Number of pixels	256	512	512	-	
Package length	31.75	40.6	31.75	40.6	mm
Number of pins	2	2	2	-	
Window material	Fiber op	tic plate	Fiber op	-	
Weight	8.0	10.0	8.0	10.0	g

⇒ Specifications (Ta=25 °C)

Parameter	Symbol	S3901-F series			9	I I too i b		
		Min.	Тур.	Max.	Min.	Тур.	Max.	Unit
Pixel pitch	-	-	50	-	-	25	-	μm
Pixel height	-	-	2.5	-	-	2.5	-	mm
Spectral response range (10% of peak)	λ	360 to 1000			360 to 1000			nm
Peak sensitivity wavelength	λр	-	600	-	-	600	-	nm
Photodiode dark current*3	ID	-	0.2	0.6	-	0.1	0.3	рА
Photodiode capacitance*3	Cph	-	20	-	-	10	-	pF
Saturation exposure*3 *4	Esat	-	200	-	-	200	-	Mlx · s
Saturation output charge*3	Qsat	-	50	-	-	25	-	pC
Photo response non-uniformity*5	PRNU	-	-	±5	-	-	±5	%

^{*3:} Vb=2.0 V, V ϕ =5.0 V

^{*2:} No condensation

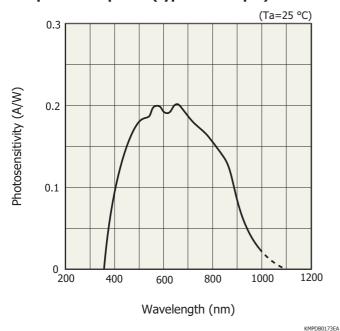
^{*4: 2856} K, tungsten lamp
*5: 50% of saturation, excluding the start pixel and last pixel

■ Electrical characteristics (Ta=25 °C)

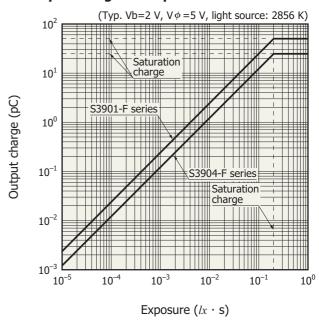
Parameter		Symbol	Condition	S3901-F series			S3904-F series			Unit
				Min.	Тур.	Max.	Min.	Тур.	Max.	Offic
Clock pulse (\phi1, \phi2) voltage	High	Vф1, Vф2 (H)		4.5	5	10	4.5	5	10	V
	Low	Vφ1, Vφ2 (L)		0	-	0.4	0	-	0.4	V
Start pulse (¢st) voltage	High	Vøst (H)		4.5	Vø1	10	4.5	Vф1	10	V
	Low	Vøst (L)		0	-	0.4	0	-	0.4	V
Video bias voltage*6		Vb		1.5	Vф - 3.0	Vф - 2.5	1.5	Vф - 3.0	Vφ - 2.5	V
Saturation control gate volta	ge	Vscg		-	0	-	-	0	-	V
Saturation control drain volta	ige	Vscd		-	Vb	-	-	Vb	-	V
Clock pulse (\phi1, \phi2) rise / fal	l time*7	trø1, trø2 tfø1, tfø2		-	20	-	-	20	-	ns
Clock pulse (\$1, \$2) pulse wi	dth	tpw\psi1, tpw\psi2		200	-	-	200	-	-	ns
Start pulse (\psi st) rise / fall time		trøst, tføst		-	20	-	-	20	-	ns
Start pulse (\pst) pulse width		tpwøst		200	-	-	200	-	-	ns
Start pulse (φst) and clock pu (φ2) overlap	ılse	tφον		200	-	-	200	-	-	ns
Clock pulse space*7		X1, X2		trf - 20	-	ı	trf - 20	-	-	ns
Data rate*8		f		0.1	-	2000	0.1	-	2000	kHz
Video delay time		tvd	50% of saturation *8 *9	-	120 (-256 F)	-	-	150 (-512 F)	-	ns
				-	160 (-512 F)	-	-	200 (-1024 F)	-	ns
Clock pulse (\phi1, \phi2) line capacitance		Сф	5 V bias	-	36 (-256 F)	-	-	50 (-512 F)	-	pF
				-	67 (-512 F)	-	-	100 (-1024 F)	-	pF
Saturation control gate (Vscg	1)	Coop	5 V bias	-	20 (-256 F)	-	-	24 (-512 F)	-	pF
line capacitance		Cscg	J V DIAS	-	35 (-512 F)	-	-	45 (-1024 F)	-	pF
Video line constitues		CV	2 V bias	-	11 (-256 F)	-	-	16 (-512 F)	-	pF
Video line capacitance		CV	Z V DIdS	-	20 (-512 F)	-	-	30 (-1024 F)	-	pF

^{*6:} V\(\phi\) is input pulse voltage.

Spectral response (typical example)



- Output charge vs. exposure



KMPDB0099EB

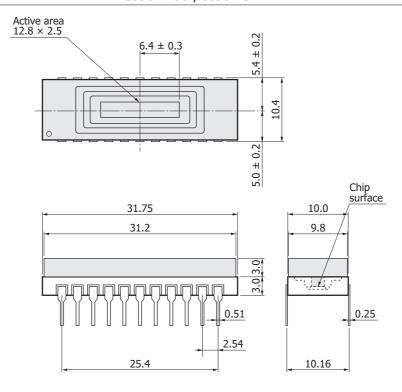
^{*7:} trf is the clock pulse rise or fall time. A clock pulse space of "rise time/fall time - 20" ns or more should be input if the clock pulse rise or fall time is longer than 20 ns.

^{*8:} Vb=2.0 V, V ϕ =5.0 V

^{*9:} Measured with C7883 driver circuit.

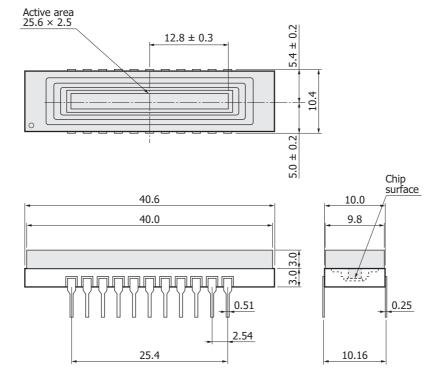
Dimensional outlines (unit: mm)

S3901-256F, S3904-512F



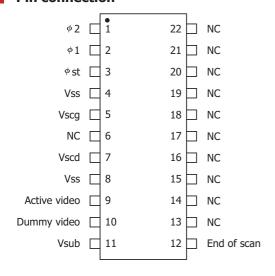
KMPDA0093E

S3901-512F, S3904-1024F



KMPDA0094EB

Pin connection



Vss, Vsub and NC should be grounded.

KMPDC0056EA

Related information

www.hamamatsu.com/sp/ssd/doc_en.html

- Precautions
- · Disclaimer
- · Image sensors

Information described in this material is current as of March 2017.

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